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PATENT NUMBER

36 852

**O.J.P.E.**

**PATENT DATE**

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Q.A. 

APPLICATION NO. 09/965841	CONT/PRIOR	CLASS 327 361	SUBCLASS 767	ART UNIT 2827 2816	EXAMINER DINH
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## APPLICANTS

Thomas Glenn  
Steven Webster

### TITLE

Integrated circuit package including interconnection posts for multiple electrical connections

PTO-2040  
12/99[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig. <b>8B</b>	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>  	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  	_____ (Primary Examiner) (Date)		<b>ISSUE FEE</b> Amount Due Date Paid	
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		<b>ISSUE BATCH NUMBER</b>	

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